

PETsys
Electronics

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PETsys TOF ASIC evaluation kit

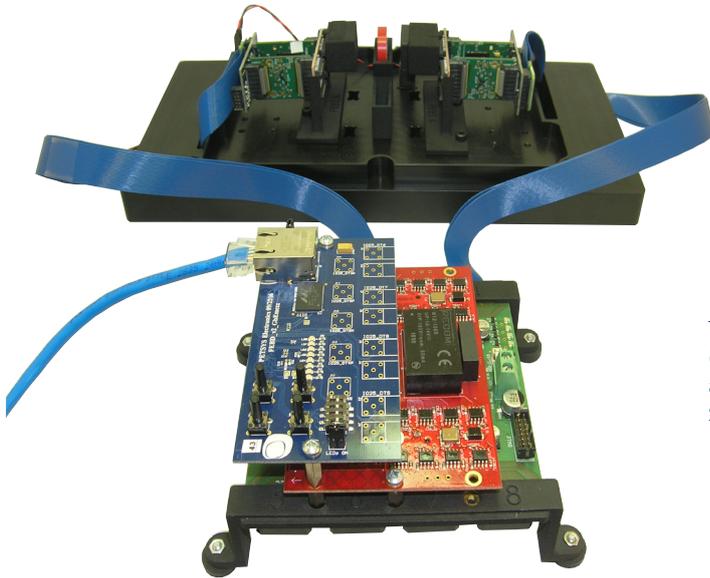


Figure 1: TOF ASIC evaluation kit.

The PETsys TOF ASIC evaluation kit consists of a FEB/D board (bottom) with Ethernet link to the computer, and a mechanical assembly (top) with two FEM128 front end modules, two KETEK SiPM pixels, two LYSO crystals and a holder for placing a ^{22}Na source in between.

The PETsys TOF ASIC evaluation kit (Fig. 1) is a complete readout kit allowing to read 256 SiPM channels. It provides an easy to use, and cost effective, way for exploring the unique, powerful and versatile features of the PETsys TOFPET2 ASIC (Fig. 2) [1]. It is also the perfect solution for anybody needing to read a few 100 SiPM channels.

The evaluation kit comes with a FEB/D board with Ethernet link to the computer, and two FEM128 front end modules (Fig. 3). Each FEM128 has two ASICs reading 64 channels each. The FEM128 modules have connectors that directly accept the Hamamatsu S13361-350Ax-08 8x8 MPPC array, or the KETEK PA3325-WB-0808 array. With a suitable adapter board, the FEM128 will equally well read analog SiPM arrays from other vendors.

Flexible cables connect the FEM128 boards to the FEB/D board, providing great flexibility in the experiment setting.

The evaluation kit is provided with firmware and software. The data acquisition software runs under Linux and comes with an easy to use graphical user interface, see figure 5. The software is written in Python and C++, and is also provided as source code, allowing the advanced user to customise it.

For an easy start we supply the evaluation kit with two KETEK SiPM pixels mounted on a board that plugs in the connector on the TOFPET2 ASIC test board, two LYSO crystals of $3 \times 3 \times 5 \text{ mm}^2$, and a holder for placing a standard circular ^{22}Na source in between. We also include a cooling box with two Peltier elements. The box provides a stable temperature environment allowing reproducible measurements, and also protects the measurement setup from ambient light.

Included in the price of the evaluation kit there is a hands-on and individual training for one or two people. The training introduces the user to the use of the evaluation kit, the readout electronics and the software.

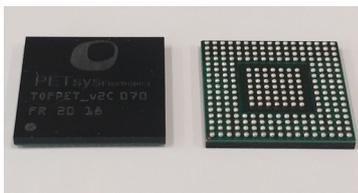


Figure 2: PETsys TOFPET2 ASIC.
The 64-channel ASIC is available as a BGA package.

[1] Experimental characterization of the TOFPET 2 ASIC, R. Bugalho et al. JINST_079P_0918

The following items are included in the evaluation kit:

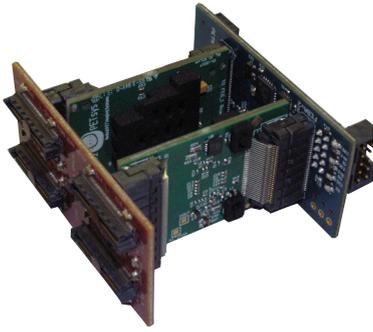


Figure 3: FEM128 module.

The FEM128 module has two ASICs, and has connectors accepting a Hamamatsu S13361-3050Ax-08 MPPC 8x8 array, or a KETEK PA3325-WB-0808 array. With the e-kit we standard ship two FEM128.

1. Two FEM128 modules (Fig. 3), and two cables for connecting the modules to the FEB/D board.
2. One FEB/D board reading the data and sending the data to the DAQ computer using a 1 Gbit Ethernet link. The FEB/D also provides regulated voltages, clock and reset signals to the ASICs, and bias voltages to the SiPMs.
3. Two SiPM pixels (KETEK 3x3mm) mounted on boards that plug into the FEM128 modules, two LYSO crystals and a radioactive source (^{22}Na) holder.
4. A cooling box with two Peltier elements providing protection against ambient light and allowing stable and reproducible temperature conditions (Fig. 4).
5. Firmware for the FEB/D board, allowing configuration of the ASICs, and readout of the data using Gigabit Ethernet. The FEB/D board is supplied with the firmware installed.
6. JTAG interface cable for programming the firmware in the Xilinx FPGA of the FEB/D board. This allows to upload firmware updates in the FEB/D.
7. Application software for receiving the data in a PC.
8. One day training on the use of our readout electronics for one or two people.
9. Documentation.



Figure 4: Cooling box.

A box with two Peltier cooling elements provides protection against ambient light and allows stable and reproducible temperature conditions.

Not part of the standard evaluation kit package but needed:

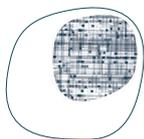
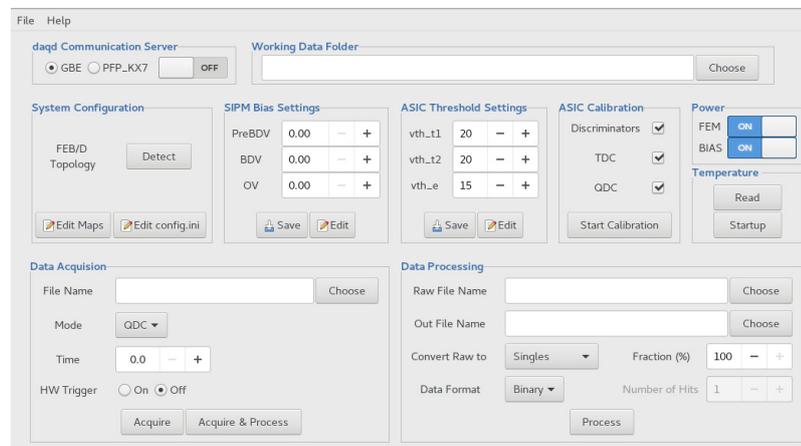
1. Power supply: 12 Vdc, 4 A
2. PC computer running Linux RHEL/CentOS 6.x or 7.x, 64-bit.
3. A ^{22}Na source if the user wants to measure the coincidence time resolution with 511 keV gamma rays.

Possible options:

1. We can supply LYSO crystal arrays, BGO crystal arrays and SiPM arrays.
2. We can supply the DAQ computer with all application software installed.
3. We can provide additional FEM128 modules. The e-kit can read up to 8 FEM128.

Reading more than 1'024 channels requires a DAQ board and a Clock&Trigger module. See the flyer about the PETSys SiPM readout system for more information about possible options for reading systems with more than 1'024 channels.

Figure 5: Graphical user interface.
The data acquisition is controlled by an easy to use graphical user interface.



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For more information contact

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